Applicants:

UNITED STATES PATENT AND TRADEMARK OFFICE

Kalnitsky, et al.

Atty Docket:

60305-307201

Application No:

09/882,538

Group Art Unit:

2811

COPY OF PAPERS
ORIGINALLY FILED

Filed:

June 15, 2001

Examiner:

Farahani, D.

Title:

METHOD FOR FORMING A SUPER SELF-ALIGNED HETEROS
JUNCTION BIPOLAR TRANSISTOR

Commissioner of Patents Washington, D.C. 20231

FORMAL DRAWINGS TRANSMITTAL

Enclosed herewith for filing in the above-identified application are the following:

Letter to the Official Draftsman;

Formal Drawings (9 Sheets); and

Postcard for date-stamped return as confirmation of receipt of these items.

Total are no fees due at this time. However, the Commissioner is further authorized to charge any required additional fees, or credit any overpayment, to deposit account 02-3964.

Respectfully submitted,

DATE: March 7, 2002

Brian R. Coleman

Reg No: 39,145

OPPENHEIMER WOLFF & DONNELLY LLP

Customer No. 25696

P.O. Box 10356

Palo Alto, CA 94303

Telephone (650) 320-4000

Fax (650) 320-4100

CERTIFICATE OF MAILING (37CFR 1.8 (a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited on March 7, 2002 with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231.

Date: March 7, 2002

Sheena Hicks

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Attn: Official Draftsman Commissioner of Patents Washington, D.C. 20231

LETTER TO THE OFFICIAL DRAFTSMAN

Dear Sir:

Subject to the approval of the Examiner, please substitute the enclosed 9 sheets of formal drawings for the informal drawings originally filed with the application.

Respectfully submitted,

DATE: March 7, 2002

Brian R. Coleman Reg. No: 39,145

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Date: March 7, 2002

Sheena Hicks